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Response

4/5/03

WRI

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Shunpei YAMAZAKI et al.

Serial No. 09/916,484

Filed: July 30, 2001

For: ELECTRIC DEVICE, MATRIX)

DEVICE, ELECTRO-OPTICAL DISPLAY)

DEVICE, AND SEMICONDUCTOR)

MEMORY HAVING THIN-FILM)

TRANSISTORS)



Art Unit: 2823

Examiner: S. Foong

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with The United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on 3-28-2003

Adele M. Stamper
Adele M. Stamper

RESPONSE TO RESTRICTION REQUIREMENT

Honorable Commissioner of Patents

Washington, D.C. 20231

Sir:

In response to the election requirement in the Office Action of February 26, 2003, Applicant hereby elects without traverse Species 1 of the claimed invention including forming at least two p-channel transistors in series wherein an off current from each of the p-channel thin film transistors is less than 10^{-12} A where a voltage of the drain region is 1 V. Claims 2-6, 8-12, and 14-39 read on the elected Species 1.

Examination on the merits is requested.

Respectfully submitted,

Eric J. Robinson
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